

DOCUMENT-IDENTIFIER:

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TITLE:

device and method for

Active matrix substrate, display
driving the display device

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Detail Description Paragraph - DETX (67):

[0125] The line sequential addressing is carried out on the driver shown in FIG. 6A. In the driver shown in FIG. 6A, data is temporarily stored on data storage capacitors 22 and then switching elements (or switches) 23 are turned ON at a time, thereby supplying a signal onto a plurality of data lines simultaneously. Each of these switching elements 23 may be a CMOS circuit having a structure such as that shown on the right-hand side of FIG. 6B. Specifically, the CMOS circuit is formed of a circuit component, in which p- and n-channel transistors are connected in parallel to each other, as a unit. Control signals with mutually opposite polarities are input to these transistors, thereby turning one of these transistors ON and the other transistor OFF at the same time. Accordingly, the potential rise and drop, caused just after these transistors have been switched, are canceled.